

2954/OJ588

TRANSMITTAL LETTER TO THE UNITED STATES
DESIGNATED/ELECTED OFFICE (DO/EO/US)

09/869991

INTERNATIONAL APPLICATION NO.
PCT/EP00/00133INTERNATIONAL FILING DATE
07 January 2000PRIORITY DATE CLAIMED
08 January 1999

TITLE OF INVENTION

ARRANGEMENT OF ELECTROCHEMICAL CELLS AND CIRCUIT BOARD

APPLICANT(S) FOR DO/EO/US

Uffe NORTOFT; Michael Thorby JORGENSEN; Ole Stig NISSEN

Applicant herewith submits to the United States Designated/Elected office (DO/EO/US) the following items and other information:

1. ☒ This is a **FIRST** submission of items concerning a filing under 35 U.S.C. 371.
2. ☐ This is a **SECOND** or **SUBSEQUENT** submission of items concerning a filing under 35 U.S. C. 371.
3. ☐ This is an express request to begin national examination procedures (35 U.S.C. 371 (f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S. C. 371 (b) and PCT Articles 22 and 39 (1).
4. ☒ A proper Demand for International Preliminary Examination was made by the 19th month from the earliest claimed priority date.
5. ☒ A copy of the International Application as filed (35 U.S. C. 371 (c) (2))
 - a. ☐ is transmitted herewith (required only if not transmitted by the International Bureau).
 - b. ☒ has been transmitted by the International Bureau
 - c. ☐ is not required, as the application was filed in the United States Receiving Office (RO/US)
6. A translation of the International Application into English (35 U.S. C. 371 (c)(2)).
7. Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371 (c)(3))
 - a. ☐ are transmitted herewith (required only if not transmitted by the International Bureau).
 - b. ☐ have been transmitted by the International Bureau.
 - c. ☐ have not been made; however, the time limit for making such amendments has NOT expired.
 - d. ☐ have not been made and will not be made.
8. A translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371 (c) (3)).
9. ☒ An oath or declaration of the inventor(s)(Unexecuted) (35 U.S.C. 371(c)(4)).
10. ☒ A translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371 (c)(5)).

Items 11. to 16. below concern other document(s) or information included:

11. ☐ An Information Disclosure Statement under 37 CFR 1.97 and 1.98 (with reference).
12. ☐ An assignment document for recording. A **separate** cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.
13. ☒ A FIRST preliminary amendment.
☐ A SECOND or SUBSEQUENT preliminary amendment.
14. ☐ A substitute specification.
15. ☐ A change of power of attorney an/or address letter.
16. ☐ Other items or information:

EXPRESS MAIL CERTIFICATE

Date 7/9/01 Label No. 6270674459812

I hereby certify that, on the date indicated above, this paper or fee was deposited with the U.S. Postal Service & that it was addressed for delivery to the Assistant Commissioner for Patents, Washington, DC 20231 by "Express Mail Post Office to Addressee" service.

Name (Print)

Signature

U.S. APPLICATION NO. (if known) 37 CFR 1.50) **09/869991**

INTERNATIONAL APPLICATION NO.: PCT/EP00/00133

Attorney's Docket Number
2954/OJ588

17. [x] The following fees are submitted:

Basic National Fee (37 CFR 1.492 (a)(1)-(5)):

Search Report has been prepared by the EPO ☐ or JPO ☒

\$860.00

International preliminary examination fee paid to USPTO (37 CFR 1.482)

\$690.00

No international preliminary examination fee paid to USPTO (37 CFR 4.482)
but international search fee paid to USPTO (37 CFR 1.445 (a) (2)...

\$710.00

Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO.....

\$1,000.00

International preliminary examination fee paid to USPTO (37 CFR 1.482)
and all claims satisfied provisions of PCT Article 33(2)-(4).....

\$100.00

\$ 860.00

Surcharge of \$130.00 for furnishing the oath or declaration later than ☐ 12 ☐ 130 months from the earliest claimed priority date (37 CFR 1.492(e)).

\$

Claims	Number Filed	Number Extra	Rate		
Total Claims	28-20	8	8 X \$18.00	\$144.00	
Independent Claims	3-3	0	0 X \$80.00	\$0.00	

Multiple dependent claims(s) (if applicable)

+ 270

\$

TOTAL OF ABOVE CALCULATIONS =

\$1004.00

Reduction by 1/2 for filing by small entity, if applicable. Verified Small Entity statement must also be filed. (Note 37 CFR 1.9, 1.27, 1.28).

\$0.00

SUBTOTAL =

\$1004.00

Processing fee of \$130.00 for furnishing the English translation later than ☐ 20 ☐ 39 months from the earliest claimed priority date (37 CFR 1.492(f)).

\$

+

TOTAL NATIONAL FEE =

\$1004.00

Fee for recording the enclosed assignment (37 CFR 1.21(h)). the assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31). \$40.00 per property

\$0.00

+

TOTAL FEES ENCLOSED =

\$1004.00

Amount to be
refunded

\$

charged

\$

a. ☒ A check in the amount of \$1004.00 to cover the above fees is enclosed.

b. ☐ Please charge my Deposit Account No.04-0100 in the amount of \$ to cover the above fees.

c. ☒ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 04-0100. A duplicate copy of this sheet is enclosed.

NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status.

SEND ALL CORRESPONDENCE TO.

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SIGNATURE



NAME S. Peter Ludwig

REGISTRATION NO. 25,351

09/869991
JC18 Rec'd PCT/PTO 09 JUL 2001

EXPRESS MAIL CERTIFICATE

Date 7/9/01 Label No. 64706744598Us

I hereby certify that, on the date indicated above, I deposited this paper or fee with the U.S. Postal Service and that it was addressed for delivery to the Commissioner of Patents & Trademarks, Washington, DC 20231 by "Express Mail Post Office to Addressee" service.

DB Peck [Signature]
Name (Print) Signature

File No.: 2954/OJ588US0

DARBY & DARBY P.C.

805 Third Avenue
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Date: July 9, 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of: Uffe NORTOFT et al.

Serial No: To be assigned
(U.S. National Phase of
International Application No. PCT/EP00/00133
Filed: 07 January 2000)

Filed: Concurrently herewith

For: ARRANGEMENT OF ELECTROCHEMICAL CELLS AND CIRCUIT
BOARD

PRELIMINARY AMENDMENT

Hon. Commissioner of
Patents and Trademarks
Box PCT
Washington, DC 20231
Attn: DO/EO/US

Sir:

Prior to examination, please amend the above-identified application
as follows:

09869991-09201
T00260-16669860

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Patents and Trademarks
July 9, 2001
Page 2

IN THE CLAIMS:

Please cancel claims 1-28 and add the following new claims:

29. (New) A cell unit comprising at least two flat electrochemical cells joined by flexible connections to at least one edge of a circuit board, the cells being moveable from a first position laterally of the circuit board to a second position arranged against one or both sides of the circuit board, whereby the circuitry on the circuit board is protected.

30. (New) A cell unit according to claim 29, wherein the cells and the circuit board have the same lengths and widths.

31. (New) A cell unit according to claim 29, wherein circuitry is provided on only one side of the circuit board and the cells are arranged against that one side or on both sides.

32. (New) A cell unit according to claim 29, wherein circuitry is provided on both sides of the circuit board and the cells are arranged against both sides of the board.

33. (New) A cell unit according to claim 29, wherein the cells and the circuit board are square or rectangular.

34. (New) A cell unit according to claim 29, wherein cells are provided on two or more edges of the circuit board and optionally two cells are connected at the same edges of the board.

35. (New) A cell unit according to claim 29, wherein the circuit board includes voltage equalising components, and/or temperature sensing components and/or charge control circuitry.

36. (New) A cell unit according to claim 29, wherein each cell is sealed within sealing material, the material protruding at the end of the cell which is connected to the circuit board such that sealing material is arranged both on top of and below the circuit board to protect the electrical connections between the cell and the circuit board.

37. (New) A cell unit according to claim 36, wherein the protruding sealing material is fixed to the circuit board.

38. (New) A cell unit according to claim 37, wherein the sealing material is fixed through one or more apertures in the circuit board.

39. (New) A cell unit according to claim 36, wherein the cells are sealed within a sealing material and any sealing material protruding at an edge of the cell, other than that edge which is to be connected to the circuit board, is folded over onto the surface of the sealed cell, such folded sealed edges then forming a spacer when the cell is folded onto the circuit board.

40. (New) A cell unit according to claim 29, wherein the circuit board is a flexible circuit board.

41. (New) A cell unit according to claim 40, wherein the circuit board can itself fold, and in particular the flexible circuit board has a rectangular shape and can be folded in half.

42. (New) A cell unit comprising one or more than one flat electrochemical cell and a circuit board, wherein the cell has terminals which are connected at one edge of the circuit board, and are sealed within a sealing material which protrudes at the terminal end of the cell, and wherein the protruding sealing material encloses the edge of the circuit board and is fixed or bonded to the circuit board.

43. (New) A cell unit according to claim 42, wherein the protruding sealing material is bonded to itself through one or more perforation/s or apertures in the circuit board.

44. (New) A cell unit according to claim 42, wherein the protruding sealing material is bonded to the edge of the circuit board by gluing, taping or heat sealing.

45. (New) A cell unit according to claim 42, wherein the circuit board and/or the cell is square or rectangular in shape.

46. (New) A cell unit according to claim 42, wherein the or each cell is moveable from a first position in the same plane as the circuit board to a second position arranged against the circuit board.

47. (New) A cell unit according to claim 42 wherein the circuit board is foldable.

48. (New) A method of producing a cell unit comprising at least two flat electrochemical cells and a circuit board, wherein the cells are arranged laterally of the circuit board and are electrically connected thereto, the method comprising the step of folding the cells onto one or both sides of the circuit board whereby the circuitry on the circuit board is protected.

49. (New) A method according to claim 48, wherein the cells and the circuit board are square or rectangular.

50. (New) A method according to claim 49, wherein cells are provided on two or more edges of the circuit board and optionally two cells are connected at the same edges of the board.

51. (New) A method according to claim 48, wherein each cell is sealed within sealing material, and the material at the end of the cell which is connected to the circuit board protrudes such that sealing material is arranged both on top of and below the circuit board to protect the electrical connections between the cell and the circuit board.

52. (New) A method according to claim 51, wherein the protruding sealing material is fixed to the circuit board.

53. (New) A method according to claim 52, wherein the sealing material is fixed through one or more apertures in the circuit board.

54. (New) A method according to claim 48, wherein the cells are sealed within a sealing material and any sealing material protruding at an edge of the cell, other than that edge which is to be connected to the circuit board, is folded over onto the surface of the sealed cell, such folded sealed edges then forming a spacer when the cell is folded onto the circuit board.

55. (New) A method according to claim 48, wherein the circuit board is a flexible circuit board.

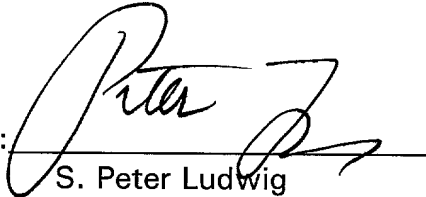
56. (New) A method according to claim 55, wherein the flexible circuit board has a rectangular shape and is folded in half.

Hon. Commissioner of
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July 9, 2001
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REMARKS

Claims 1-28 are cancelled. Claims 29-57 are new. No new matter is added as a result of the amendment.

Respectfully submitted,

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ARRANGEMENT OF ELECTROCHEMICAL CELLS AND CIRCUIT BOARD

5 The present invention relates to an arrangement of electrochemical cells, in particular an arrangement of batteries or capacitors which are connected in series or in parallel and which include additional electrical circuit elements or additional electrical circuitry.

10 In recent years there have been large advances in the design and production of batteries. In particular, lithium-ion batteries provide many advantages over conventional nickel cadmium batteries. Lithium-ion batteries can be formed by flattened "jelly-rolls" of interleaved electrodes and electrolyte, the thus formed electrical cell being packaged in a flexible pack formed, for example, from thin laminated foil material. The ability to seal the cells within flexible housings, rather than the traditional rigid cans, is based on the fact that the lithium-ion batteries use a gelled or solid polymer electrolyte. The problems relating to leakage, which are known in connection with conventional batteries, do thus not arise.

25 The widths and lengths of polymer, lithium-ion cells can be freely selected, as can the thickness of the cells. This flexibility of design, and the flexibility of the finished cell, makes polymer lithium-ion cells particularly suitable for use in portable telephones and computers, as well as in other electronic devices where space is at an absolute premium. It will be well understood that a compromise must often be reached between the energy to be delivered by a battery and the space available for the battery.

35 Examples of lithium-ion cells construction and the use of flexible packages for such cells are described in more detail in, for example, WO-A-97/03475 (Danionics), EP-A-

0845821 (Sanyo), US 5609974 (Battery Engineering), US 4997732 (MHB), US 5478668 (Bell), WO-A-95/13629 (Valence), US 5591540 (Motorola), US 5716421 (Motorola) and US 5445856 (Gill).

The operational voltage of lithium-ion batteries is typically 3 to 4 volts. On the other hand, the voltage demand of many electronic devices is 8 to 9 volts, or some other multiple of the individual cell voltage. Thus, it is commonplace for lithium-ion cells to be arranged one adjacent the other, and connected in series. Alternatively, the cells can be placed one on top of the other, and again connected in series. In different situations, it is possible that a higher capacity is required, for the same voltage, in which case a number of cells could be connected in parallel.

When a number of cells are connected in series, it is generally necessary to provide some voltage equalizing components across each cell, to avoid the problem of the charging voltage being applied unequally between the individual cells, which would result in an unequal load on the individual cells so reducing the performance and lifetime of the cell. The voltage equalizing components are typically resistors. However, it is also commonplace for other electrical components to be connected to the cells, e.g. capacitors, integrated circuits for charge control, state of charge indicators, memory elements, disconnect switches and other associated control circuits. Usually, such additional circuit elements and circuitry are arranged at the end of the stack or row of individual cells, adjacent the terminals. The additional circuit elements and circuitry obviously need some protection and this is usually done by providing a hard box for the arrangement of cells and circuit elements, contacts being provided on the external surface of the hard box.

However, while in such arrangements a saving of space is achieved relative to traditional cells, there is an ever present demand for further space saving so that the maximum energy density can be achieved, particularly for cells to be used in mobile phones etc.

It should be emphasized that the present application, and the issues discussed above, apply equally to electrochemical capacitors as to batteries. The design of such electrochemical capacitors is similar to that of lithium-ion cells. An example of such a capacitor - sometimes known as a "super capacitor" - is described in more detail in US Patent 5646815 (Medtronic) or in EP-A-0625787 (Matsushita). Generally speaking, electrochemical capacitors, either based on the double layer principle or on the pseudo-capacitance principle, differ from batteries in providing high discharge rates over a short length of time, whereas batteries are better suited to providing a power output over a longer period of time. Electrochemical capacitors also tend to be able to be recharged many more times than batteries. In some situations, the capacitors can replace batteries and in other situations they can be used together with batteries, for example as load levelling devices.

The capacitors can also be used as memory back-up elements in electronic devices, and in mobile transmitting equipment, for example in connection with mobile telephones where there is a demand for high current rate pulses. In this context, therefore, a combination of capacitors and high density battery cells is often required.

The operational voltage of a double layer supercapacitor is typically 2 to 3 volts. For the same reasons as discussed above in relation to battery cells, it is frequently necessary to connect a number of electrochemical capacitors together, in series or in

parallel. As indicated above, it is also often necessary to connect a combination of battery cells and electrochemical capacitors together. Again, these groups of cells will tend to include electrical components and electronic circuits, which typically would be housed altogether in a hard box, thus providing protection for the circuitry.

Electrochemical cells, either batteries or capacitors, are often produced as relatively thin square or rectangular cells, as described in the above-mentioned US patent 5445856. In the present application these cells are called "flat cells" and this term is intended to cover cells whose thickness is substantially less than the width and/or length of the cells. Because of the production method of the cells - often by winding strips of electrodes and electrolyte - the cells will be square or rectangular in shape. The term "flat cells" will thus normally be understood to mean relatively thin square or rectangular cells, though the invention is not limited to a square or rectangular shape.

In US 5637418, an electrochemical cell stack is described in combination with a flexible circuit board. In the patent, the circuit board is folded around the electrochemical cell stack.

US-A-5367431 describes a thin power supply unit with flat power supply elements bonded onto a flexible circuit board. In one embodiment, power supply elements are arranged on both sides of the circuit board. Electrical connections between the elements and the circuit board are established by means of openings in the cases of the elements.

It is an object of the invention to provide an arrangement of electrochemical cells and associated

electrical components and/or electronic circuitry in which the said components/circuitry can be protected and wherein a relatively high saving of space can be achieved.

According to the invention there is provided a cell unit including at least two flat electrochemical cells which are connected with additional electrical components and are connected together, the cells being folded such that the external terminals of the cell unit are exposed but that the additional elements are positioned between the cells. The components may be positioned between the faces of the cells, when folded together, or between the edges of the cells.

In another aspect of the invention, there is provided a cell unit which includes at least two flat electrochemical cells and a circuit board, the cells being folded onto one or both sides of the circuit board whereby the circuitry on the circuit board is protected. If the circuitry is provided on only one side of the circuit board then the cells can be folded only onto that one side. If circuitry is provided on both sides of the circuit board then preferably the cells are folded onto both sides of the board.

Preferably, the cells and the circuit board are square or rectangular. Cells can be provided on two or more sides of the circuit board and two cells can be connected at each side of the board.

It will be understood that the flat cells which are housed in relatively soft flexible packaging provide a high degree of protection to the circuit elements or circuitry, particularly bearing in mind that the relative flexibility of the cells means that any shocks transferred to the cell unit are at least partly absorbed and are not fully transferred to the circuit elements or circuitry on the

circuit board.

Preferred embodiments of the present invention are described in more detail below, by example only, and with reference to the accompanying drawings wherein:

Fig. 1a illustrates at an arrangement of electrochemical cells according to a first embodiment of the invention.

Fig. 1b is a schematic perspective view of the arrangement of Fig. 1a, in a folded condition.

Fig. 2a shows an arrangement of electrochemical cells according to a second embodiment of the present invention.

Fig. 2b is a schematic perspective and partly sectional view of the arrangement of Fig. 2a, in a folded condition.

Fig. 3a shows an arrangement of electrochemical cells according to a third embodiment of the present invention.

Fig. 3b is a schematic perspective and partly sectional view of the arrangement of Fig. 3a, in a folded condition.

Fig. 4a is an exploded view of an arrangement of electrochemical cells according to a fourth embodiment of the invention.

Figs. 4b, 4c and 4d are detail views illustrating the connections of the arrangement of the cells of Fig. 4a.

Fig. 4e is a schematic perspective and partly sectional view of the arrangement of Fig. 4a, in a folded condition.

Fig. 5a illustrates at an arrangement of electrochemical cells according to a fifth embodiment of the invention.

Fig. 5b is an exploded view of an arrangement of electrochemical cells according to the fifth embodiment of the invention.

Fig. 5c is a detail view illustrating the connections of the arrangement of the cells of Fig. 5a.

Fig. 5d is a schematic perspective and partly sectional view of the arrangement of Fig. 5a, in a folded condition.

Fig. 6a is a schematic perspective view of a sixth embodiment of the invention.

Fig. 6b is a perspective view of the arrangement of Fig. 6a, in a folded condition.

Figs. 6c and 6d are enlarged details of the perspective view of Fig. 6b.

Fig. 1 shows a pair of flat electrochemical cells 1, 1' connected in series. The cells, which may be lithium-ion batteries or capacitors, are flat cells housed in flexible packages. The exact design structure of the cells is not relevant to the present application, but they may be as described in US patent number 5445856, i.e. flat wound cells housed in a thin foil laminate package.

The cells 1, 1' are identical, but cell 1' is the other way up to cell 1 so that when the two cells are placed next to each other with the symmetrically arranged terminals in contact, the positive terminal 2 of cell 1 will be in contact with the negative terminal 2' of cell 1'. In other words, the cells are connected in series. The other terminals 3, 3' of the cells provide the external connection points for the cell pair.

Connected across the terminals of each cell are electrical components 4, 4'. The components 4, 4' are, for example, resistors to provide for voltage equalizing between the cells, in a known and conventional manner. The electrical components 4, 4' can be flat or cylindrical, with a minimum thickness, and lie on the surface of the cells 1, 1'. For example, the electrical components may be formed of standard resistors, e.g. metal film resistors.

When the cells 1, 1' are folded together, the

electrical components 4, 4' are arranged between the cells, preferably offset relative to one another. Thus, the components do not protrude from the cell pair and are protected between the cells. It will be understood that the thickness of the components between the cells is small (for example 0.5-2mm) and so the overall thickness of the cell pair will not be increased to any significant extent.

The terminals 2, 2' are folded against the side of the cells 1, 1' when the cell pair is folded. Thus, the only parts protruding from the cell pair are the external terminals 3, 3'. The fact that the electrical components do not protrude from the cell pair, and are arranged within the envelope of the cell perimeters, means that a real saving in space has been achieved. Furthermore, the fact that the electrical components are protected between the cells means that the cell pair does not automatically require a hard box for the protection of the electrical components. This possibility of avoiding the use of a hard box again provides for a real potential space saving.

A variation on the above concept is shown schematically in Fig. 1b. Instead of the components being located between the faces of the cells, in Fig. 1b the components are located at the end of the cells, between the terminals. Flat cells of the type described here do not have "square" ends but rather the terminal ends where the packaging material is sealed are tapered or rounded, creating a recess between adjacent folded cells into which the electrical components can be positioned. Again, the components do not protrude from the cell perimeter and are at least partly protected by the packaging material of the cells. Furthermore, the space taken up by the cell pair is not increased to any significant degree.

It will be appreciated by those skilled in the art that the connection of the cell terminals and the

electrical components can be easily achieved in an automatic or semi-automatic manufacturing line, by welding or soldering. The terminals are of thin flexible metal foil, as can be the connections for the electrical components themselves.

The embodiment of Fig. 2a is similar to the embodiment of Fig. 1a in that there is a pair of electrochemical cells 1, 1'. However, in this embodiment, there is also provided a circuit board 5 between the cells. The circuit board has six terminals, two terminals 2, 3 facing cell 1, two terminals 2', 3' facing cell 1' and two terminals 6, 6' along a third side for the external connection of the cell arrangement. The circuit board 5 includes the resistors 4, 4' in a conventional fashion.

The circuit board is fabricated in a known manner. For example, the circuit board can be a flexible circuit board, e.g. a so-called "flex print circuit board", where the circuit is printed onto a flexible isolating substrate, and then preferably coated by a similar substrate. Specifically, the circuit board could be formed by etching a copper-clad flexible board having a conductor of copper and a base film of polyamide.

The electrical connection tabs on the circuit board are arranged on the edge of the board, so that they correspond in position to the tabs of the electrochemical cells and can easily be connected thereto, without risk of damage to the sensitive battery or circuit components.

As will be apparent from Fig. 2a, the circuit board is of the same shape as the cells 1, 1'. The circuit board will also be formed with a minimum thickness. The fact that the resistors 4, 4' are integrated with the circuit board makes the welding or soldering of the cells 1, 1' easier. The arrangement of three adjacent and similarly

shaped parts (1, 1' and 5) makes an automated connecting process relative easy to apply. Thus, the terminals of cells 1, 1' are connected to the circuit board 5 in a corresponding manner to that described in relation to Fig. 1a.

An advantage of the embodiment of Fig. 2a is that after welding/soldering, the terminals of the cells are fixed relative to each other, so that handling of the cell unit is easy with a low risk of damage to the connection points, for example through twisting. Moreover, the embodiment of Fig. 2a has the advantage that the terminals for the external connection can be freely placed, anywhere on the circuit board 5. This freedom of design of the external connection points means that the cell unit can be designed to fit with the electrical device in which it is to be used, rather than the intended electrical device being designed around the cell.

As shown in Fig. 2b, the cells 1, 1' can be folded relative to the circuit board 5, so that the circuit board 5 is sandwiched in between the cells. In this way, the circuitry on the circuit board is completely protected between the cells. In an alternative arrangement, not illustrated, both cells could be folded on top of the circuit board; again, the circuitry of the board would be protected, though the board itself would have to have a degree of rigidity because its rear surface would be exposed.

Needless to say, the length of metal foil connections between the cells and the circuit board is selected so as to permit the folding of the cells in their chosen position.

The components on the circuit board 5 are generally conventional. For example, they may be so-called thick

film chip resistors, having a thickness of approximately 0.5mm. The board could include any electrical circuit, for example a charging circuit, or any components relevant in connection with battery and capacitor technology.

A specific example of a suitable component is a temperature sensitive element. It is known to monitor the temperature of electrochemical cells with such elements, in order to ensure that over-heating does not occur and/or to ensure that the charging or re-charging of the cell is regulated appropriately. If over-heating does occur, a switch on the charger can be tripped.

A typical temperature sensor is a resistor with a positive temperature coefficient, a so-called "PTC element". The location of such a temperature sensor on the circuit board has the advantage that the sensor is immediately adjacent the cell so that a good thermal contact is achieved. Where the cells are folded on both sides of the board, a very efficient temperature monitoring is achieved as one sensor can monitor both cells. Furthermore, no additional wiring of the sensor to the circuit board is necessary.

The size and shape of the circuit board can be freely selected, in accordance with the size and shape of the cells. For example, the circuit board could if desired be twice as long as each cell, so that each cell folded on to half of the circuit board. Again, the circuitry on the board would be protected by the folded cells.

A thin and flexible circuit base or board could also be formed of metal - in particular copper - foils separated and supported by insulating tape or thin plastic foils. On this base the electrical components can be fixed and connected.

5 The embodiment of Fig. 3a is similar to the embodiment of Fig. 2a, in that a circuit board 5 is included. However, in this embodiment there is an additional cell 1'', so that a cell is provided on each of three sides of the circuit board, the three cells being in series. As in the embodiment of Fig. 2a, the circuit board includes appropriate electrical components or circuitry, for example resistors, and the cells are welded or soldered to the circuit board by means of flat, bendable terminals. The external terminals 6,6' of the cell unit are provided on the fourth side of the circuit board.

15 In an arrangement where there are three cells on three sides of the circuit board, obviously a square shape of the circuit board is most convenient. The cells are folded so that preferably two are on top of the circuit board and the third cell folded beneath the board. The circuit board is thus concealed and protected between the cells. This is schematically illustrated in Fig. 3b.

20 It can be mentioned here that an embodiment with three cells will find many practical applications, given that the voltage of each cell will typically be approximately 2 to 3 volts for capacitor cells and 3 to 4 volts for lithium-ion battery cells and the voltage demand of many electrical devices is approximately 6 to 9 volts.

25 It should also be mentioned that a fourth cell could be provided at the fourth edge of the circuit board 5. The external terminals can still be arranged on that edge, as these will be accessible when the fourth cell is folded onto the board. Alternatively, the external terminals could be formed as wires, leading out from a corner of the cell unit. In another possibility, all of the cells - two, three or four cells - could be folded onto either the top or bottom of the circuit board and the terminals could be provided on the other surface of the board.

It should be emphasized that the connections illustrated in the Figs. 2 and 3 are not limiting. Another possibility for connecting more cells is to place cells with terminals on top of each other, connect the flexible terminals and then fold the cells against the board.

In the embodiments of Figs. 2 and 3, the circuit board is one-sided. Providing a two-sided circuit board, as illustrated in relation to the embodiment of Fig. 4a, provides even greater design flexibility. In particular, as illustrated in Figs. 4a to 4e, four cells can be connected to the circuit boards, two cells on each of two opposing edges, leaving the external terminals 6, 6' to be provided along one of the other edges of the circuit board.

The exploded view of Fig. 4a shows unconnected cells 1, 1' with the terminals arranged to one side. The cells 1, 1' are different ways up so that the terminals of cell 1 are provided to one side and the terminals of cell 1' are provided to the other side. The cells 1, 1' can thus be placed one on top of each other, to the right side of the circuit board 5, with their terminals not overlapping but instead connecting to the appropriate circuit paths on the board as illustrated.

On the left hand side of Fig. 4a, a pair of cells 1'', 1''' is shown, connected to the board, the lower terminals 7, 8 being of the upper cell 1'' and the upper terminals 9, 10 being of the cell 1''' beneath.

Fig. 4b shows in more detail the circuit board of Fig. 4a. The resistors 4 provided between the terminal connections for each cell are shown. In an alternative embodiment, the resistors could be temperature sensing elements as described above. It will be well understood that the four cells are connected in series, with the negative terminal of cell 1' being connected to the

positive terminal of cell 1'', the negative terminal of cell 1'' being connected to the positive terminal of cell 1''' etc.

5 In Fig. 4c are shown the circuit paths on the circuit board 5 for the external connection of the series of cells, together with the connection of cell 1' to cell 1''. Fig. 4d shows the connection of cell 1 to cell 1' as well as the connection of cell 1'' to cell 1'''. The dashed rectangular panel in Figs. 4b, 4c and 4d represents an insulating sheet 11 to separate the circuitry of Fig. 4c from the circuitry of Fig. 4d.

10 The completed cell package is formed by folding the two cells on one side of the circuit board 5 on top of that board and folding the two cells on the opposite side below the circuit board. This arrangement is illustrated schematically in Fig. 4e. Alternatively, a cell from each side would be folded on top and beneath the board.

15 An additional advantageous feature is illustrated in Fig. 4e. The sealed edges 11 or "fins" of the flat cells are folded onto that surface of the cells which, when the cells are folded onto the circuit board, will be abutting the board. In this way, the foot-print of the cell package is reduced, in that the edges are folded inwards, and furthermore the folded in edges provide a slight spacing of the cells from the board. This spacing provides a convenient accommodation of the components on the board and of the connecting tabs, and also a greater degree of protection to these components and tabs, as the pressure thereon is reduced.

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35 In the embodiment of Fig. 4a, it will be appreciated that each of the cells is of the same design. In contrast, in the embodiment of Fig. 5a, the cell of each pair on each opposing side of the circuit board 5 is different from the

other cell of the pair, as will be appreciated from a consideration of the exploded view of Fig. 5b. The terminals of the cell 1' shown in Fig. 5b are reversed as compared to the terminals of the cell 1. This reversal of the positive and negative terminals of one of the cells provides for simpler series interconnection of the cells, as shown in particular in Fig. 5c, which illustrates the circuit board 5 of Fig. 5a. Thus, the crossing of the connections which occurs in relation to the circuit board of Fig. 4 is avoided, since the negative and positive terminals of the cell pairs 1 and 1', and 1'' and 1''' are adjacent. This simpler interconnection of the cells provides the immediate advantage that a one-sided circuit board can be used.

Fig. 5c also shows the resistors 4 between the terminals of each cell. In an alternative embodiment, the resistors could be temperature sensing elements as described above.

As in the embodiment of Fig. 4, the cells can be folded on either side of the circuit board, preferably two on top and two below as shown in Fig. 5d.

The sixth embodiment of the invention which is illustrated in Figs. 6a, 6b, 6c and 6d has some similarities with the embodiment of Fig. 3 in that there are three electrochemical cells which are folded relative to the circuit board. However, whereas in the embodiment of Fig. 3 the three cells are arranged on three sides of a substantially square circuit board, in the embodiment of Fig. 6 the circuit board is rectangular. The width of the rectangular circuit board is approximately the length of one of the electrochemical cells and the length of the circuit board is approximately twice the width of the electrochemical cells (which may be either square or rectangular).

A rectangular circuit board as illustrated in Fig. 6a could have, instead of three or fewer cells, four or more electrochemical cells connected thereto, with sets of cells folded next to each other so that the resulting arrangement remains rectangular, matching the shape of the circuit board. However, the embodiment of Fig. 6 is a further modification of a basic rectangular circuit board because the circuit board is itself intended fold in half.

The circuit board is indicated in Fig. 6 by the reference numeral 5. There are three cells 1', 1'' and 1'''. The circuit board includes components 4, 4', for example temperature sensing elements. Terminals 6, 6' are for external connection of the sealed arrangement, after folding. The terminals 2, 2' of cell 1' are seen connected to the circuit board, in a conventional fashion, and it will be understood that the terminals of the other cells are connected in the same way.

As discussed above, the circuit can include temperature sensing elements in the form of resistors with positive or negative temperature coefficients, i.e. PTC or NTC elements. Alternatively, the board can include other components, in particular for control and safety purposes in relation to the charge and discharge of the cell arrangement.

Each electrochemical cell is of the same size and is formed in the same way, with a wound cell precursor sealed within a flexible package. In this embodiment, the sheet material forming the flexible package is folded around the wound cell precursor so as to form a seal 11 at the bottom end, a seal 12 along one flat side of the cell and a seal 13 at the top end, where the cell terminals 2, 2' or tabs emerge. The seals are closed in a conventional fashion, using heat sealing. Each cell package is thus similar to that described and illustrated in the aforementioned EP-A-

0845821 (see Fig. 17 in particular).

As discussed above in relation to Fig. 4e, the end seal 11 can be folded back on top of the sealed package in such a direction that, when the cell 1' is folded under the circuit board 5, the folded sealed edge 11 creates a slight spacing from the circuit board 5. This folded edge is seen most clearly in Fig. 6a in relation to cell 1'. Furthermore, the longitudinal seal 12, on the flat side of the cell 1', also creates a slight spacing from the circuit board 5.

At the terminal end of each cell, it is very advantageous if the material of the cell package extends beyond the seal 13 enclosing the cell terminals and is then cut along each side to provide two flaps 14. When the cell terminals 2, 2' are connected to the circuit board 5, one flap 14 will be above the circuit board 5 and the other will be below. By mechanically connecting these flaps 14 to the circuit board, a strong connection between the cell and the circuit board is achieved, so that any loads or bending stresses caused by folding of the cells will be taken by the sealing material, rather than by the cell terminals. This greatly reduces the possibility of the electrical connections between the cells and the circuit board being damaged in the folding operation. The flaps can, for example, be connected to the circuit board by gluing or taping.

In a less preferred variation of this, the flaps 14 do not need to be fixed to the circuit board but merely cover the cell terminals. The flaps still provide a measure of protection to the electrical connection between the cells and the circuit board and in particular still protect the terminals from possible short circuits.

It will be understood that in the process of welding

the terminals of the cells to the circuit board, the flaps can easily be folded back, out of the way of the welding operation, and then folded onto the circuit board, when the welding operation is completed.

A most preferred method for joining the flaps 14 of the electrochemical cells to the circuit board involves the use of a circuit board with one or more apertures or perforations. One such aperture is illustrated in Fig. 6a and indicated by reference numeral 15. The aperture or apertures is or are arranged at each side of the circuit board where the cell is to be connected and is/are positioned such that it/they will be covered by the flaps 14 at the terminal end of the cell package. The aperture 15 shown in Fig. 6a is elongate, in particular rectangular. However, a row of smaller apertures or perforations would work equally well. The material from which the cell packages are made is typically a flexible sealing laminate which comprises an inner sealing layer which allows the laminate material to be glued or sealed onto itself, in particular by heat sealing. The flaps 14 will of course be made of the same material and can contact each other through the aperture(s) 15 of the circuit board 5. Accordingly, after the cell terminals are joined to the circuit board, the flaps of the cells can very easily be joined to the circuit board by sealing the material of the flaps together, through the aperture(s). This joining of the two flaps can be achieved by gluing or heat sealing. This creates a strong and stable connection between each cell and the circuit board and ensures that any loads or forces created by the folding operation are largely taken by the material of the cell package, rather than the terminals.

The preferred means of joining the flaps of the cell packages together, through the apertures or perforations of the circuit board, is by means of heat sealing which is an

operation very suitable for automatic production.

The fixing of the cells to the circuit board in this way creates a reliable connection which ensures that the cell package keeps its desired shape, especially during the folding operation (to be described below). In addition to providing a physical protection to the cell terminals, the flaps of sealing material also protect against electrical short circuits.

As already mentioned, the circuit board is designed so that it can fold in half. The fold line is indicated by the letter X in Fig. 6a. Reference numerals Y and Z indicate the fold lines between the edges of the circuit board and the cells. It will be understood that the cells and circuit board can be folded together in a variety of different orders. One preferred order is indicated by the numbered arrows in Fig. 6a, so that in the first step the cell 1'' is folded about line Z underneath the circuit board, in the next step the cell 1' is folded about line Y on top of the circuit board, in the third step the cell 1''' is folded about line Z on top of the circuit board and in the fourth step the circuit board is folded about line X.

The resulting folded arrangement is such that cell 1'' is on the bottom and cells 1' and 1''' are between the two folded halves of the circuit board. It will be noted in particular that the circuit board terminals 6, 6' will be positioned next to each other, as a result of the folding operation. Also, the temperature sensing elements 4, 4' will be arranged between and/or on top of the folded cells.

In an alternative folding operation, in the third step the cell 1''' can instead be folded underneath the circuit board (before the circuit board is folded). With this arrangement, after the circuit board is folded, the cell

1''' is on top of the folded of the folded cell arrangement and the cell 1'' is at the bottom.

5 This particular arrangement is illustrated in the partly cut away sectional view of Fig. 6b. This view, together with the enlarged detailed views of Figs. 6c and 6d, show in particular the positioning of the heat sensing elements 4, 4' and the circuit board terminals 6, 6'. Furthermore, in Fig. 6c the folding of the ends seals 11 of the cell packages, are clearly illustrated, these folded ends seals 11 providing a slight spacing for the circuitry of the circuit board 5.

15 It should be noted that the connection of a cell to a circuit board, using the flaps of sealing material at the terminal end of the cell, with or without apertures or perforations in the circuit board, is a novel approach in cell technology. The circuit connection method is considered herein as a separate aspect of the invention and is claimed accordingly. This connection method is relevant for both multiple cell connections to a circuit board and also the connection of a single cell to a circuit board.

25 While Figs. 4b, 5c and 6a show examples of interconnecting circuitry on the circuit board 5, other connection schemes are possible and additional components can easily be including in the printed circuit board using conventional technology. The purpose of the figures is to illustrate the concept behind the present invention, not to illustrate details of circuit board design technology. It should also be emphasized that all of the cells need not be of the same size. Furthermore, combinations of serial and parallel connections can be provided on the circuit boards. As previously mentioned, combinations of batteries and capacitor cells can be used.

The size of the circuit board is obviously largely

dictated by the size of the cells, which are typically from 12 to 25mm wide and 17 to 50mm long. The cell thickness is for example between 1 and 5mm and the circuit board thickness may also be in this range, though obviously a minimum thickness is desirable, e.g. 0.5 to 2mm.

The advantages of the invention will be apparent from the above description. However, the invention is not limited to the details of the above examples of the invention.

CLAIMS

1. A cell unit which includes at least two flat electrochemical cells and a circuit board, the cells being folded onto one or both sides of the circuit board whereby the circuitry on the circuit board is protected.
2. A cell unit according to claim 1, wherein the cells and the circuit board have the same lengths and widths.
3. A cell unit according to claim 1 or 2, wherein circuitry is provided on only one side of the circuit board and the cells are folded onto that one side or onto both sides.
4. A cell unit according to claim 1 or 2, wherein circuitry is provided on both sides of the circuit board and the cells are folded onto both sides of the board.
5. A cell unit according to any preceding claim, wherein the cells and the circuit board are square or rectangular.
6. A cell unit according to any preceding claim, wherein cells are provided on two or more edges of the circuit board and optionally two cells are connected at the same edges of the board.
7. A cell unit according to any preceding claim, wherein the circuit board includes voltage equalising components, and/or temperature sensing components and/or charge control circuitry.
8. A cell unit according to any preceding claim, wherein each cell is sealed within sealing material, the material protruding at the end of the cell which is connected to the circuit board such that sealing material is arranged both on top of and below the circuit board to protect the

electrical connections between the cell and the circuit board.

9. A cell unit according to claim 8, wherein the protruding sealing material is fixed to the circuit board.

10. A cell unit according to claim 9, wherein the sealing material is fixed through one or more apertures in the circuit board.

11. A cell unit according to any preceding claim, wherein the cells are sealed within a sealing material and any sealing material protruding at an edge of the cell, other than that edge which is to be connected to the circuit board, is folded over onto the surface of the sealed cell, such folded sealed edges then forming a spacer when the cell is folded onto the circuit board.

12. A cell unit according to any preceding claim, wherein the circuit board is a flexible circuit board.

13. A cell unit according to any preceding claim, wherein the circuit board can itself fold, and in particular the flexible circuit board has a rectangular shape and can be folded in half.

14. A cell unit which includes one or more than one flat electrochemical cell and a circuit board, the or each cell having terminals which are connected at one edge of the circuit board, and the or each cell being sealed within sealing material which protrudes at the terminal end of the cell, the protruding sealing material enclosing the edge of the circuit board, and wherein the protruding sealing material is fixed or bonded to the circuit board.

15. A cell unit according to claim 14, wherein the protruding sealing material is bonded to itself through one

or more perforation/s or apertures in the circuit board.

16. A cell unit according to claim 14 or 15, wherein the protruding sealing material is bonded to the edge of the circuit board by gluing, taping or heat sealing.

17. A cell unit according to any of claims 14 to 16, wherein the circuit board and/or the or each cell is square or rectangular in shape.

18. A cell unit according to any of claims 14 to 17, wherein the or each cell is foldable onto the circuit board.

19. A cell unit according to any of claims 14 to 18 wherein the circuit board is foldable.

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1 / 11

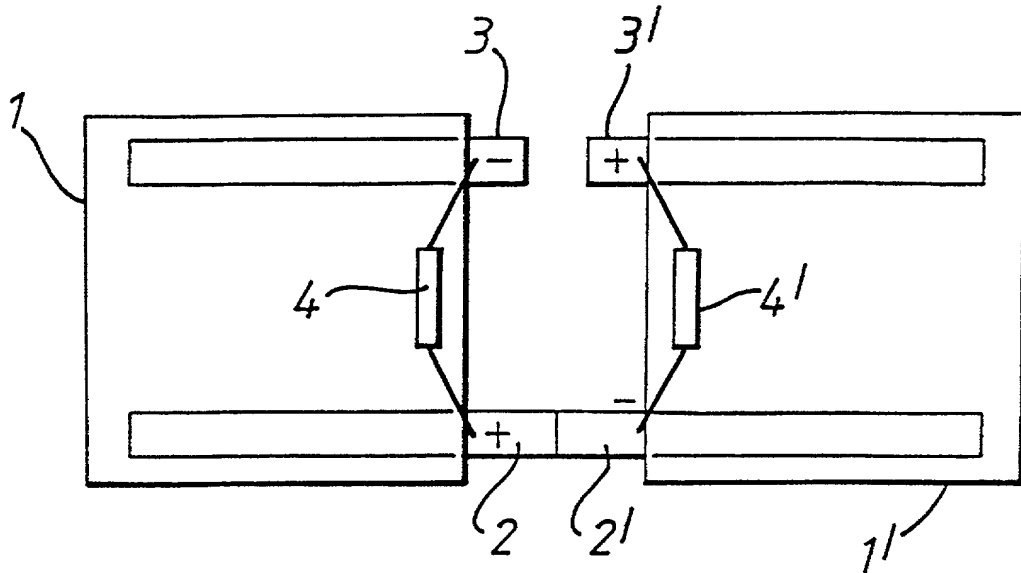


Fig. 1a

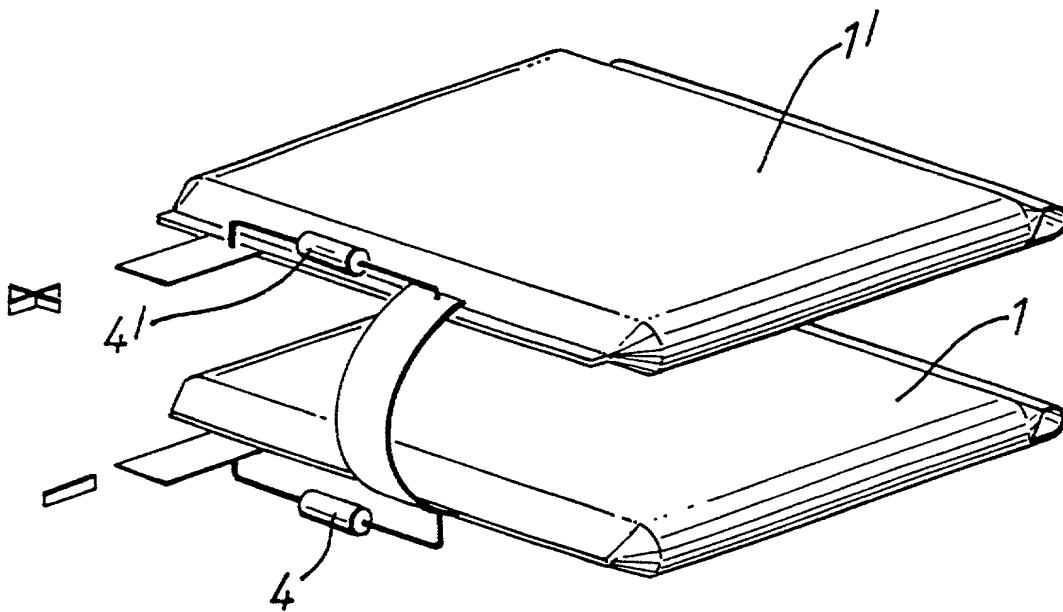


Fig. 1b

2 / 11

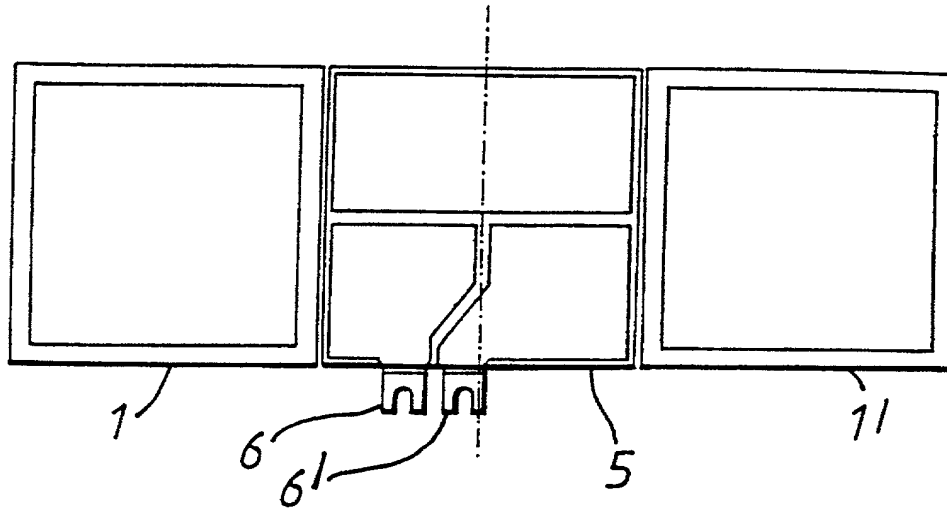


Fig. 2a

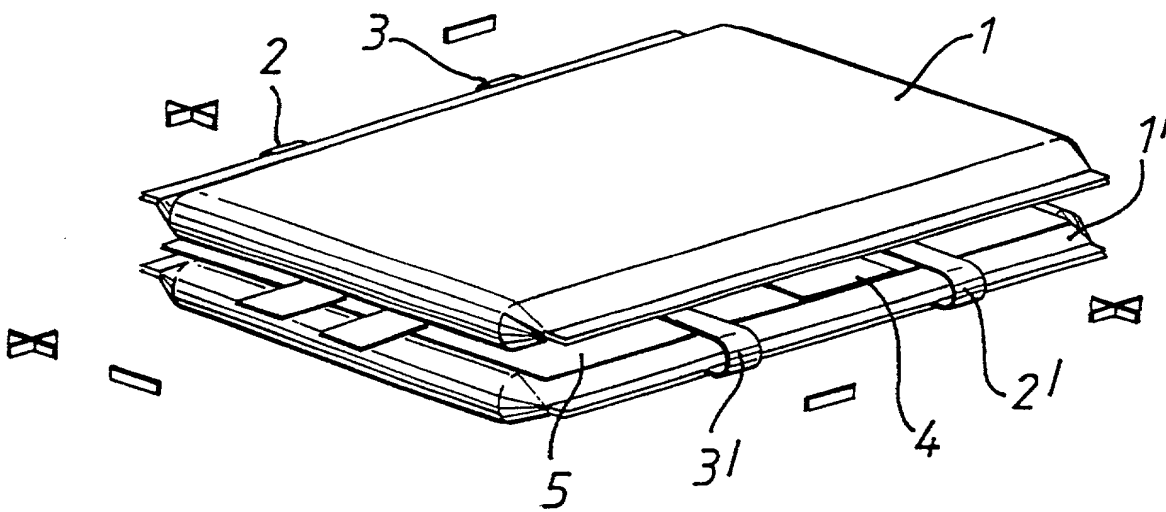
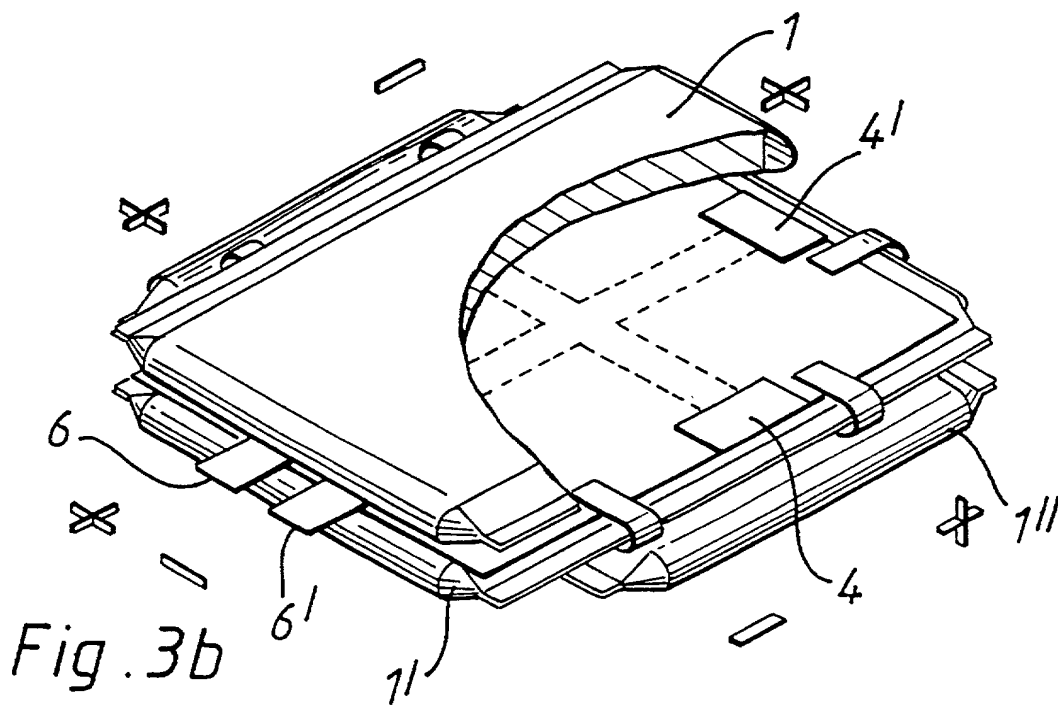
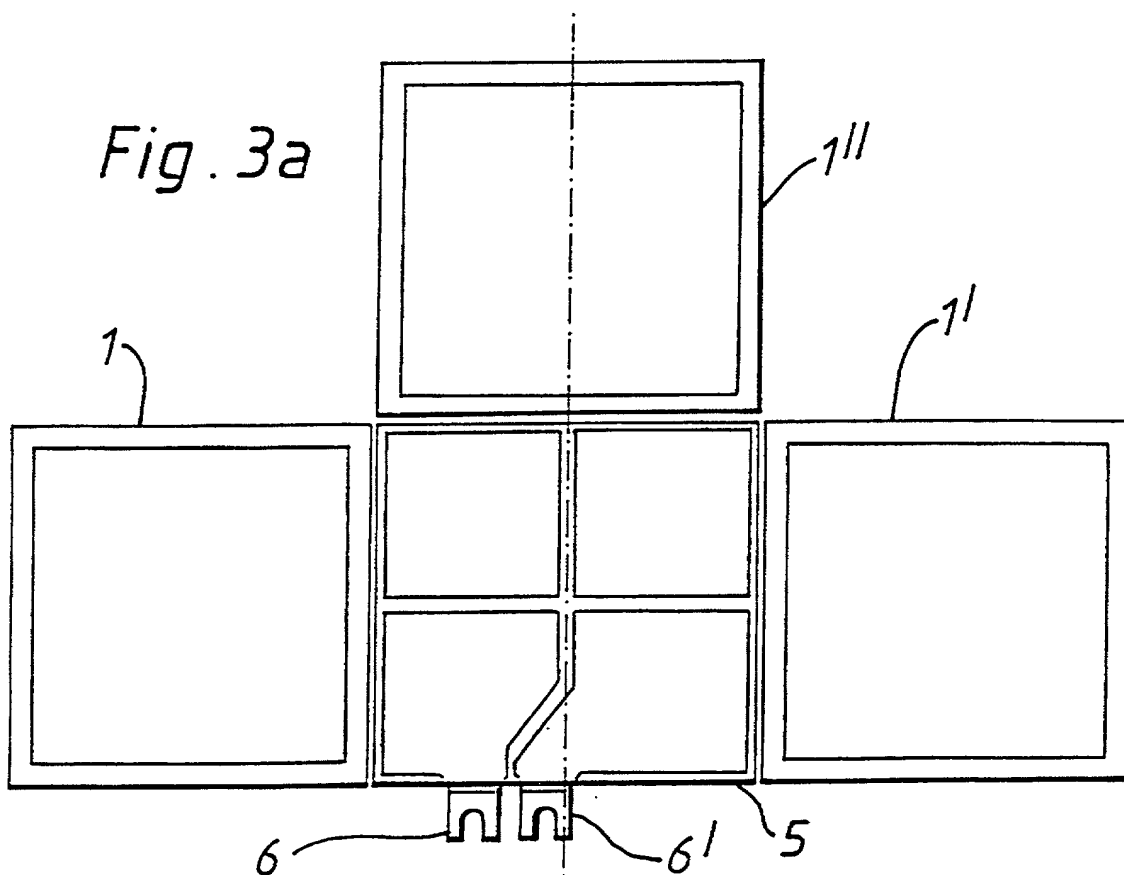


Fig. 2b

3 / 11





5 / 11

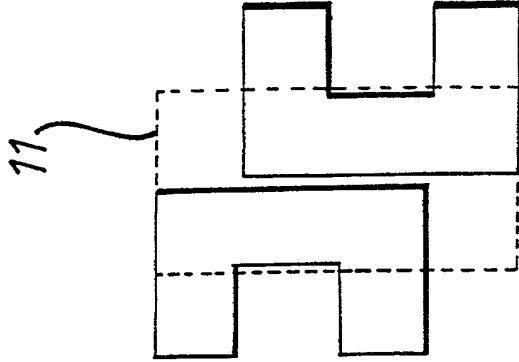


Fig. 4d

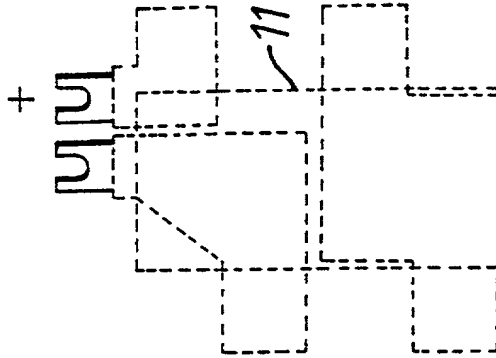


Fig. 4c

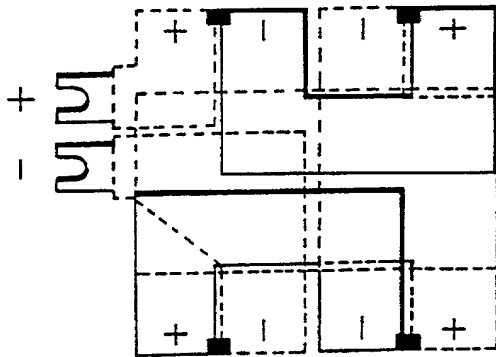


Fig. 4b

6 / 11

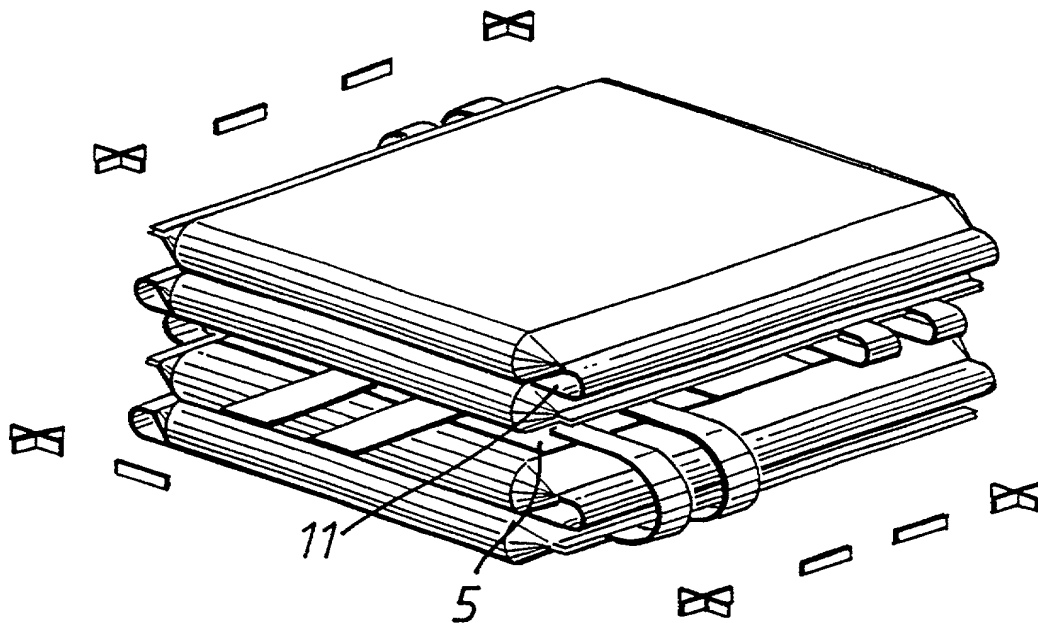


Fig. 4e

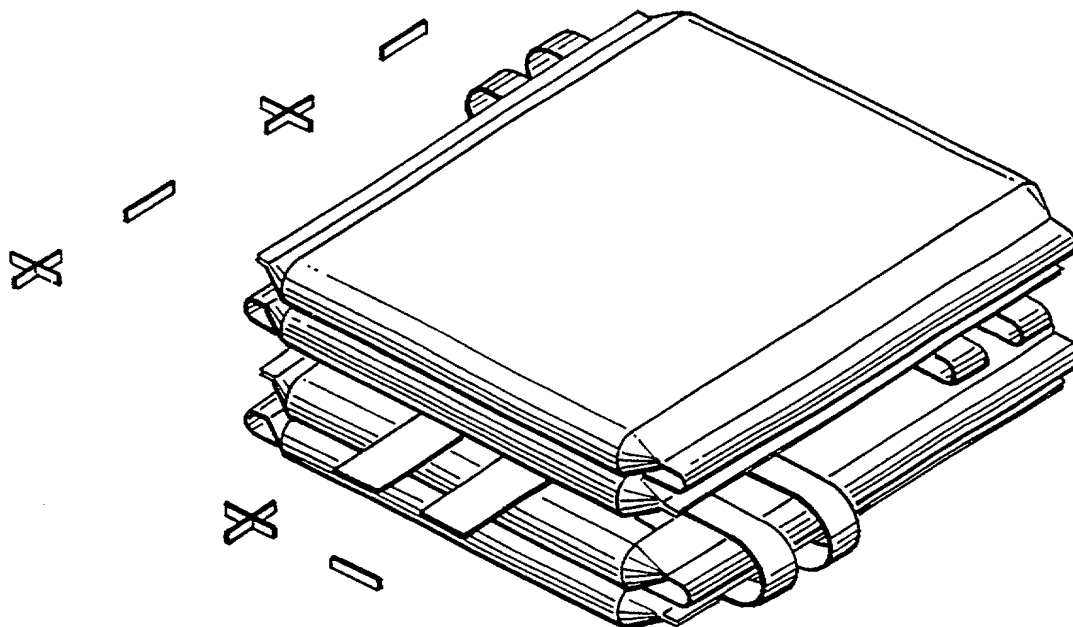


Fig. 5d

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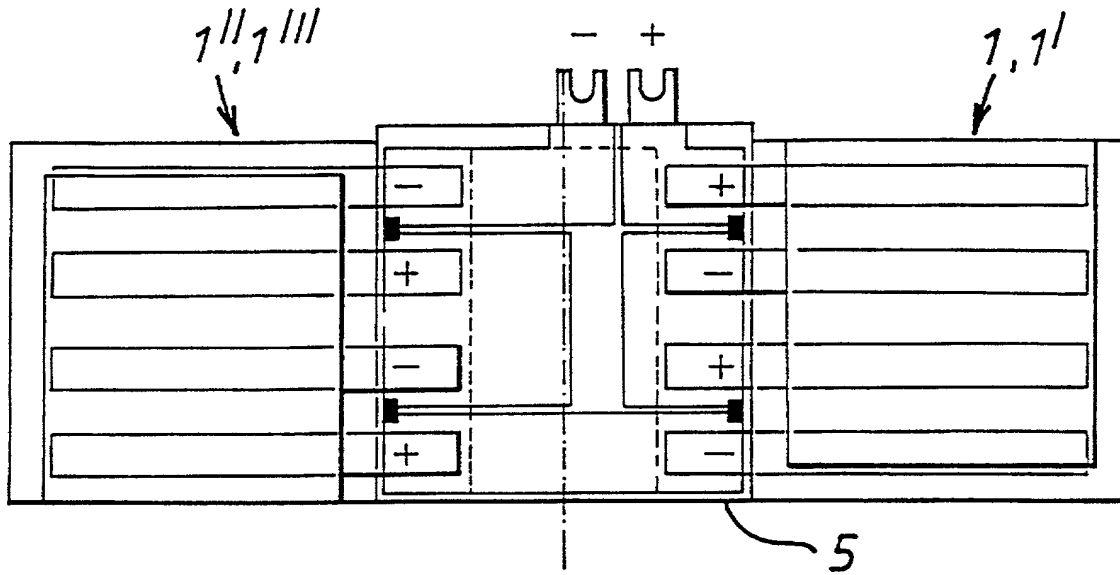


Fig. 5a

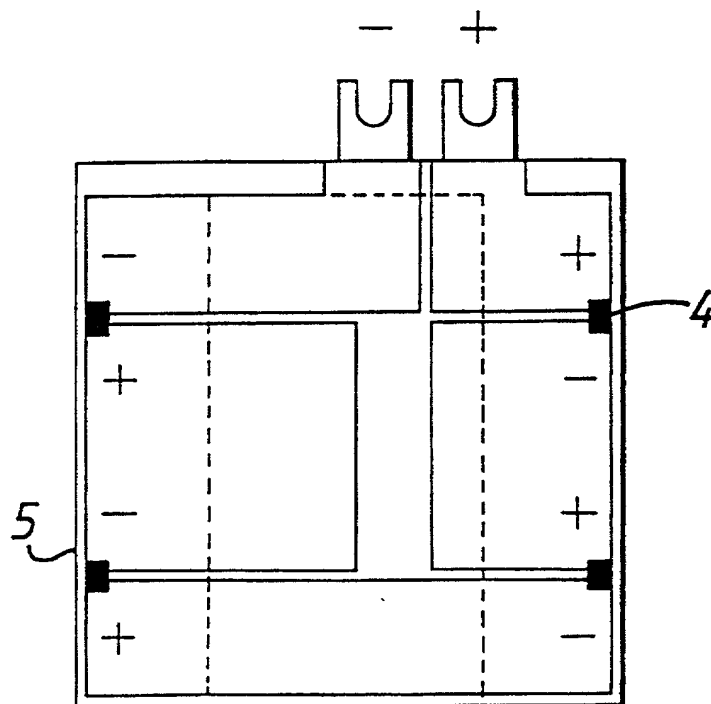
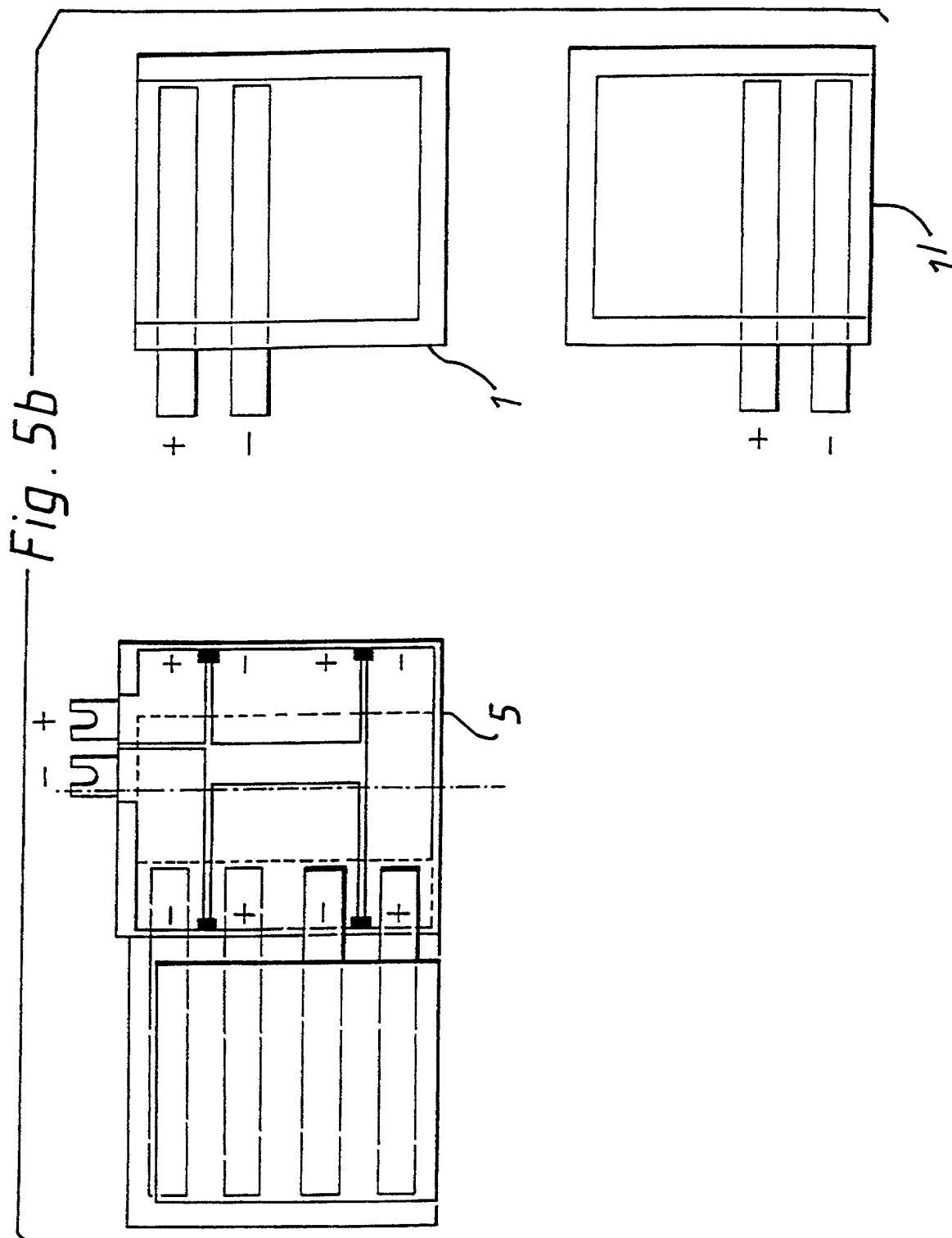


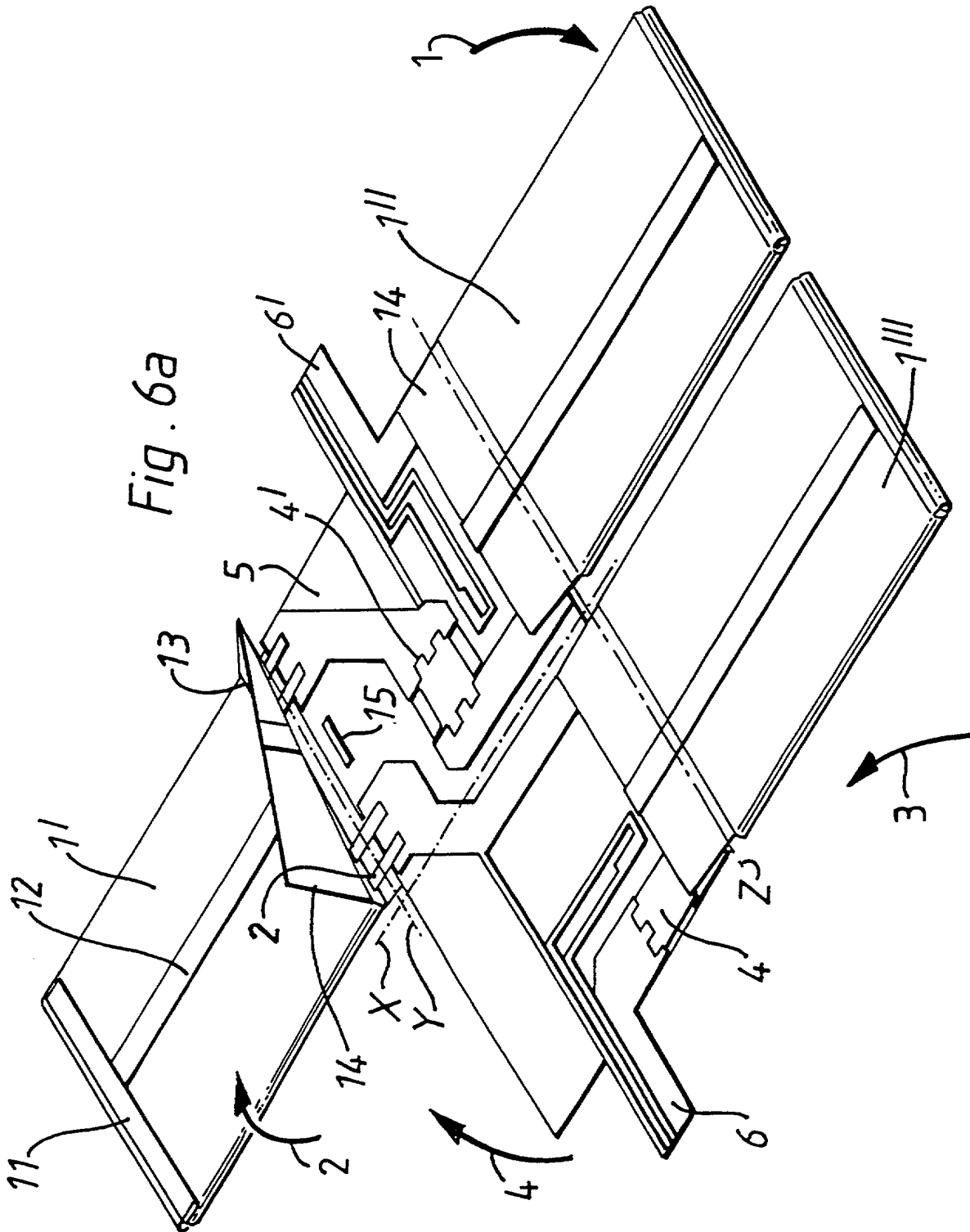
Fig. 5c

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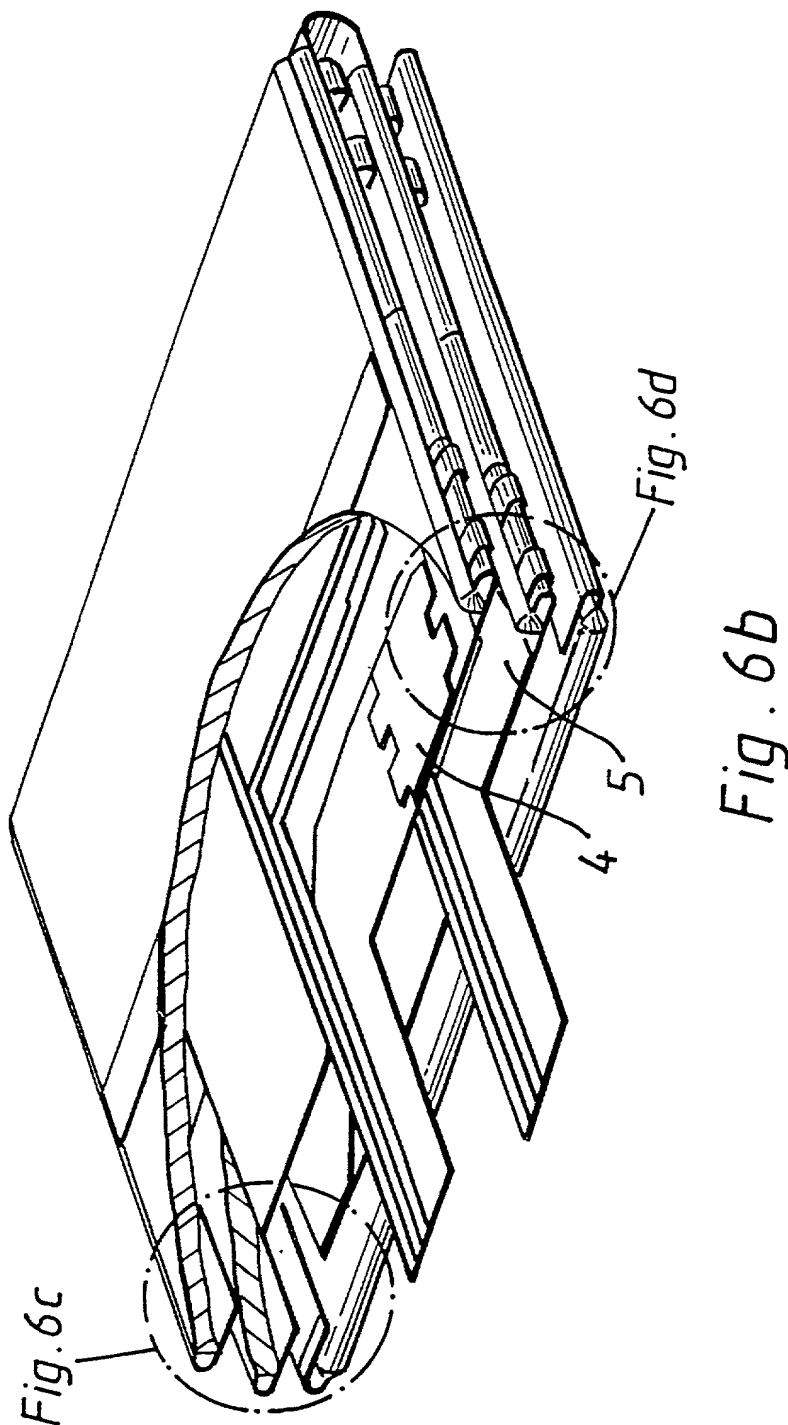


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Fig. 6a



10/11



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Fig. 6c

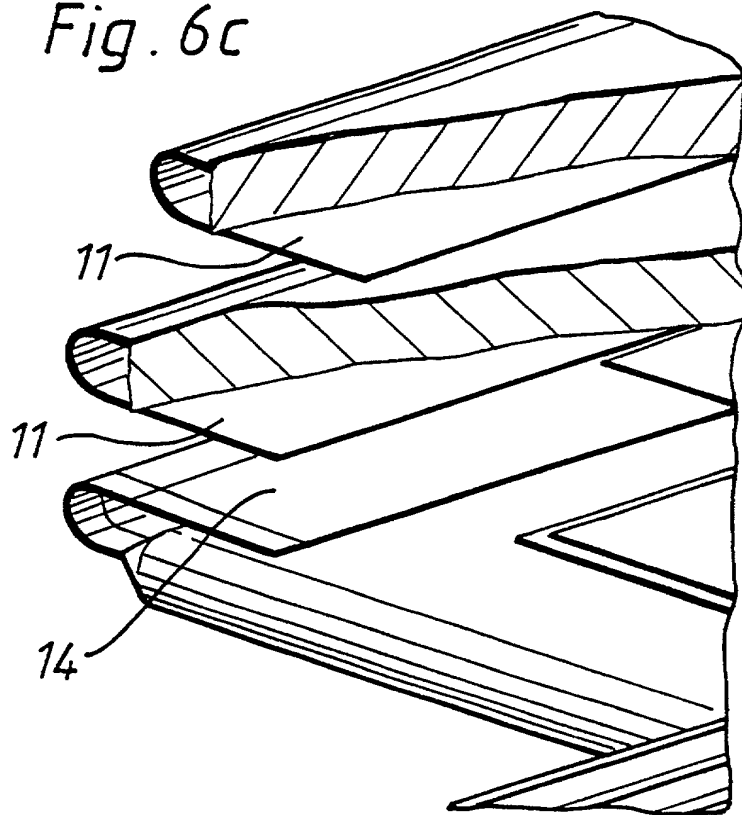
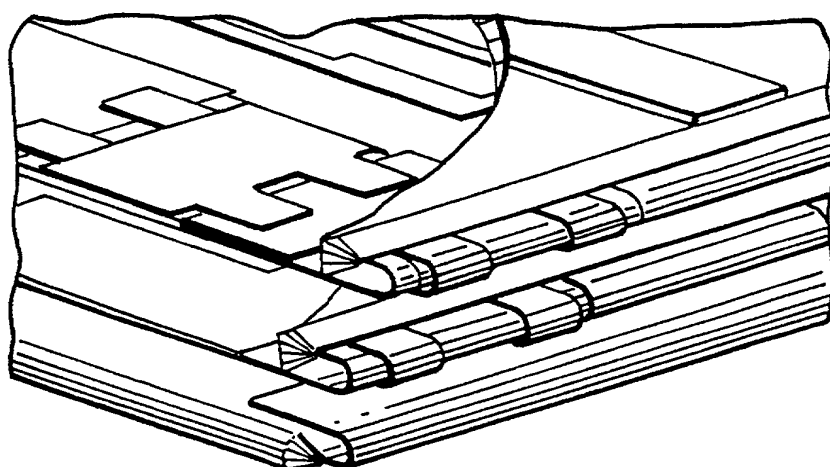


Fig. 6d



COMBINED DECLARATION FOR PATENT APPLICATION AND POWER OF ATTORNEY

(Includes Reference to PCT International Applications)

ATTORNEY DOCKET NUMBER
2954/OJ588US

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed for and which a patent is sought on the invention entitled:

ARRANGEMENT OF ELECTROCHEMICAL CELLS AND CIRCUIT BOARD

the specification of which (check only one item below):

☐ is attached hereto.☐ was filed as United States application

Serial No. _____

on _____

and was amended

on _____ (if applicable).

☒ was filed as PCT international applicationNumber PCT/EP00/00133on 07 January 2000

and was amended under PCT Article 19

on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate or of any PCT international application(s) designating at least one country other than the United States of America listed below and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed:

PRIOR FOREIGN/PCT APPLICATION(S) AND ANY PRIORITY CLAIMS UNDER 35 U.S.C. 119:

COUNTRY (if PCT indicate PCT)	APPLICATION NUMBER	DATE OF FILING (day, month, year)	PRIORITY CLAIMED UNDER 35 U.S.C. 119
Great Britain	9900396.4	8 January 1999	<input checked="" type="checkbox"/> YES <input type="checkbox"/> NO
			<input type="checkbox"/> YES <input type="checkbox"/> NO
			<input type="checkbox"/> YES <input type="checkbox"/> NO
			<input type="checkbox"/> YES <input type="checkbox"/> NO
			<input type="checkbox"/> YES <input type="checkbox"/> NO

Combined Declaration for Patent Application and Power of Attorney (Continued)

(Includes Reference to PCT International Applications)

ATTY'S DOCKET NUMBER
2954/OJ588USO

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) or PCT international application(s) designating the United States of America that is/are listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in that/those prior application(s) in the manner provided by the first paragraph of Title 35, United States Code §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior application(s) and the national or PCT international filing date of this application:

PRIOR U.S. APPLICATIONS OR PCT INTERNATIONAL APPLICATIONS DESIGNATING THE U.S. FOR BENEFIT UNDER 35 U.S.C. 120:

U.S. APPLICATIONS		STATUS (Check one)		
U.S. APPLICATION NUMBER	U.S. FILING DATE	PATENTED	PENDING	ABANDONED

PCT APPLICATIONS DESIGNATING THE U.S.		
PCT APPLICATION NO.	PCT FILING DATE	U.S. SERIAL NUMBER ASSIGNED (if any)
PCT/EP00/00133	07 January 2000	

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. Morris Relson #15,108, Gordon D. Coplein #19,165, William F. Dudine, Jr. #20,569, Michael J. Sweedler #19,937, S. Peter Ludwig #25,351, Paul Fields #20,298, Joseph B. Lerch #26,936, Melvin C. Garner #26,272, Ethan Horwitz #27,646, Beverly B. Goodwin #28,417, Adda C. Gogoris #29,714, Martin E. Goldstein #20,869, Bert J. Lewen #19,407, Henry Sternberg #22,408, Peter C. Schechter #31,662, Robert Schaffer #31,194, Robert C. Sullivan, Jr. #30,499, and Joseph R. Robinson #33,448

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S. Peter Ludwig

2 0 2 0 2	FULL NAME OF INVENTOR	FAMILY NAME	FIRST GIVEN NAME	SECOND GIVEN NAME
	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP
	POST OFFICE ADDRESS	POST OFFICE ADDRESS	CITY	STATE & ZIP CODE/COUNTRY
2 0 2 0 2	FULL NAME OF INVENTOR	FAMILY NAME	FIRST GIVEN NAME	SECOND GIVEN NAME
	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP
	POST OFFICE ADDRESS	POST OFFICE ADDRESS	CITY	STATE & ZIP CODE/COUNTRY
2 0 3	FULL NAME OF INVENTOR	FAMILY NAME	FIRST GIVEN NAME	SECOND GIVEN NAME
	RESIDENCE & CITIZENSHIP	CITY	STATE OR FOREIGN COUNTRY	COUNTRY OF CITIZENSHIP
	POST OFFICE ADDRESS	POST OFFICE ADDRESS	CITY	STATE & ZIP CODE/COUNTRY

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patents issuing thereon.

SIGNATURE OF INVENTOR 201	SIGNATURE OF INVENTOR 202	SIGNATURE OF INVENTOR 203
DATE 2001-08-24	DATE 2001-08-28	DATE 8.17.2001.